Page 1 of 1 List of Patents and Publications PADENTARY

For Applicant's Inf SERIAL NO. 09/487,529 **GROUP: 2755** For Applicant's Information Disclosure Statement (Use several sheets if necessary) FILING DATE: January 19, 2000 **U.S. PATENT DOCUMENTS** CLASS FILING DATE IF EXAM. REF. **DOCUMENT NUMBER** DATE **NAME SUB** APPROPRIATE **CLASS INITIALS** DES. **B**1 5,594,875 1/1997 Thaller 5,717,872 2/1998 Whittaker B2 **B3** 5,943,483 8/1999 Solomon PΗ **B4** 5,884,052 3/1999 Chambers et al. PH FOREIGN PATENT DOCUMENTS TRANSLATION EXAM. REF. **DOCUMENT NUMBER** DATE **COUNTRY** CLASS **SUB** YES/NO **INITIALS** DES. **CLASS** OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

B8 Anderson, "The Performance of Spin Lock Alternatives for Shared-Memory Multiprocessors," IEEE Transactions on Parallel and Distributed Systems, Vol. 1, No. 1, January 1990, pp. 6-16. MBUS Interface Specification, © 1990-1997 SPARC International, Inc. **B9** Muss hoans

IEEE Std 802.3, 2000 Edition, "Local and Metropolitan Area Networks," p. 58.

InfiniBand™ Architecture Release 1.0, October 2000, pp. 238-310.

B5

B6 B7

EXAMINER:

International Search Report, application no. PCT/US 01/01901, mailed November 5, 2001.

DATE CONSIDERED:

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.